

Technical Program Updates as of 03/03/14

Wednesday, March 5th

Updates

13:10–15:10

Lecture I

[L-1] T. Ortlepp (Invited), *CiS Research Institute / TU Ilmenau*
“Introduction to Energy-Efficient Superconducting Electronics”

Chairpersons

Masamitsu Tanaka

Lecture I

Hiroyuki Akaike

Oral Session I

Thursday, March 6th

Updates

Workshop starts at 9:50 a.m.

9:50–10:40

Lecture II

[L-2] A. Bozbey (Invited), *TOBB ETÜ*
“SFQ Analog and Digital Circuit Design: Circuit Optimization and Timing Analysis”

11:00–11:50

Lecture III

[L-3] I. Vernik (Invited), *Hypres Inc.*
“Cryogenic Memory with Magnetic Josephson Junctions”

Chairpersons

Yuki Yamanashi

Lecture II, III, and short presentations

Nobuyuki Yoshikawa

Organized Session

Friday, March 7th

Chairperson

Mutsuo Hidaka

Lecture IV and Oral Session II